

**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE**  
**UGC - AUTONOMOUS INSTITUTION**  
**CIRCULAR**

Date: 27.02.2025  
Madanapalle

All the M. Tech I Year I Semester 2024 Batch (R24) Regular M.Tech students are hereby informed that the External Practical Exams will be conducted from 01.03.2025 to 03.03.2025 as per the time table given below.

**M.Tech (VLSI & Embedded Systems) I Year I Semester (R24) Regular : 2024 Admitted Batch**

**CMOS Digital IC Design Laboratory (24VESP201)**

Date	Time	Roll No.S	No.of Students	Lab Slot
01-03-2025	09:30AM-12:30 PM	24691D8501-24691D8507, 24691D8509-24691D8515	14	EB-103

**Microcontrollers and Programmable Digital Signal Processors Laboratory (24VESP202)**

Date	Time	Roll No.S	No.of Students	Lab Slot
03-03.2025	09:30AM-12:30 PM	24691D8501-24691D8507, 24691D8509-24691D8515	14	EB-103

Note : (1) **Detained & Unregistered** students of the current semester are not eligible to write the above examinations, if in case the examination is conducted for any such student, it stands cancelled without any prior intimation

(2) if any discrepancies are found, the same may be informed to the examination section



**CONTROLLER OF EXAMINATIONS**

**CONTROLLER OF EXAMINATIONS**

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE  
(UGC - AUTONOMOUS)  
R. No 14, Kudi Road, Angali,  
Madanapalle - 517 325, AP

  
**PRINCIPAL**

**Principal**  
**Madanapalle Institute of**  
**Technology & Science**  
**MADANAPALLE**